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**at all. But digital design with VHDL stumped them: they did not have any real experience designing digital circuits, despite having taken a beginning digital design class. They were also ummox ed by the odd syntax of VHDL. Many of them resorted to trying to write VHDL as if it were C. In the spring of 2005, I made the labs more hardware-centric.**

**Experiences Teaching an FPGA-based Embedded Systems Class**

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**This book presents a step-by-step, practical approach to an enhanced and easy understanding of digital circuitry fundamentals. The author combines extensive teaching experience from his best-sellers with practical examples, in order to bring beginning learners up to speed in this emerging field. Coverage begins with the basic logic gates used to perform arithmetic operations, and proceeds up through sequential logic and memory circuits used to interface to modern PCs. MARKET: For electronic technicians, system designers, engineers.**

**For courses in Digital Electronics, Digital Systems, and Digital Design. Digital Electronics: A Practical Approach with VHDL, Ninth Edition, offers students an easy-to-learn-from resource that emphasizes practical application of circuit design, operation, and troubleshooting. Over 1,000 annotated color figures help explain circuit operation or emphasize critical components and input/output criteria. Throughout the text, the author employs a step-by-step approach that takes students from theory to example to application of the concepts. Over all nine editions, Kleitz has consistently sought out student feedback, along with his own experience of teaching the course in-class and on-line, to improve each new edition.**

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**Digital Electronics and Design with VHDL offers a friendly presentation of the fundamental principles and practices of modern digital design. Unlike any other book in this field, transistor-level implementations are also included, which allow the readers to gain a solid understanding of a circuit's real potential and limitations, and to develop a realistic perspective on the practical design of actual integrated circuits. Coverage includes the largest selection available of digital circuits in all categories (combinational, sequential, logical, or arithmetic); and detailed digital design techniques, with a thorough discussion on state-machine modeling for the analysis and design of complex sequential systems. Key technologies used in modern circuits are also described, including Bipolar, MOS, ROM/RAM, and CPLD/FPGA chips, as well as codes and techniques used in data storage and transmission. Designs are illustrated by means of complete, realistic applications using VHDL, where the complete code, comments, and simulation results are included. This text is ideal for courses in Digital Design, Digital Logic, Digital Electronics, VLSI, and VHDL, and industry practitioners in digital electronics. Comprehensive coverage of fundamental digital concepts and principles, as well as complete, realistic, industry-standard designs Many circuits shown with internal details at the transistor-level, as in real integrated circuits Actual technologies used in state-of-the-art digital circuits presented in conjunction with fundamental concepts and principles Six chapters dedicated to VHDL-based techniques, with all VHDL-based designs synthesized onto CPLD/FPGA chips**

**For Digital Electronics courses requiring a comprehensive approach to Digital concepts with an emphasis on PLD programming and the integration of the latest Quartus II software. This text presents a step-by-step, practical approach to an enhanced and easy understanding of digital circuitry fundamentals with coverage of CPLD's, VHDL and Altera's Quartus II software. Coverage begins with the basic logic gates used to perform arithmetic operations, and proceeds up through sequential logic and memory circuits used to interface to modern PCs. The author combines extensive teaching experience with practical examples in order to bring entry level students up to speed in this emerging field.**

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**A completely updated and expanded comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits. This comprehensive treatment of VHDL and its applications to the design and simulation of real, industry-standard circuits has been completely updated and expanded for the third edition. New features include all VHDL-2008 constructs, an extensive review of digital circuits, RTL analysis, and an unequalled collection of VHDL examples and exercises. The book focuses on the use of VHDL rather than solely on the language, with an emphasis on design examples and laboratory exercises. The third edition begins with a detailed review of digital circuits (combinatorial, sequential, state machines, and FPGAs), thus providing a self-contained single reference for the teaching of digital circuit design with VHDL. In its coverage of VHDL-2008, it makes a clear distinction between VHDL for synthesis and VHDL for simulation. The text offers complete VHDL codes in examples as well as simulation results and comments. The significantly expanded examples and exercises include many not previously published, with multiple physical demonstrations meant to inspire and motivate students. The book is suitable for undergraduate and graduate students in VHDL and digital circuit design, and can be used as a professional reference for VHDL practitioners. It can also serve as a text for digital VLSI in-house or academic courses.**

**This easy-to-understand book illustrates practical applications using circuits the user will face in the design engineer field. Electronics Workbench CD-ROM included contains Electronics Workbench Version 5 and EWB Multisim Version 6 circuit data files, as well as solutions to the in-text Altera and Xilinx examples-providing users with additional reinforcement and feedback concerning exercises and problems. Programmable Logic Devices (CPLDs); Timing waveforms; MultiSIM simulations of digital circuit applications; Computer generated Boolean logic reductions; Section on event counting with optical switches and Hall-effect switches; Section on connecting multiple I/O to CPLDs; Stepper motors and controller ICs. Section on implementing state machines using VHDL; and ADC and DAC simulations. For design engineers.**

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